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26824

PTO/SB/21 (08-00)

MODIFIED

TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

Application Number	09/920,240
Filing Date	08/01/2001
First Named Inventor	Pierte ROO
Group Art Unit	2682
Examiner Name	Eugene Yun
Attorney Docket Number	MP0039.CIP

	* Plus 6 cited refere	nces	Examiner Name		Eugene Yun	
Total Number of Pages in This Submission		5 *	Attorney Docket Number		MP0039.CIP	
		ENCL	OSURES	(check a	all that apply)	
Fee Transmittal Form Credit Card Authorization Amendment / Reply After Final Affidavits/declara Extension of Time Reque Express Abandonment F Information Disclosure S Certified Copy of Priority Document(s) Response to Missing Pal Incomplete Application Response to Miss under 37 CFR 1.5	tatement with Rer Rer	Assignm (for an A) Drawing Licensin Petition Petition Provisio Associa Termina Reques	nent Papers Application)		After Allowance Communication to Group Appeal Communication to Board of Appeals and Interferences Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) Proprietary Information Status Letter Other Enclosure(s) (please identify below):	
	SIGNATURE C	F APPLI	CANT, ATTORNI	Y, OR A	GENT	
Andrew J. Bateman Registration No.: 45,573						
Signature		And	m.J. Bat	ema	_	
Date 10/03/3005						
CERTIFICATE OF MAILING						
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231 on this date:						
Typed or printed name						
Signature				Date		



FEE TRANSMITTAL for FY 2005

Effective 10/01/2005. Patent fees are subject to annual revision

Applicant claims small entity status. See 37 CFR 1.27

TOTAL AMOUNT OF PAYMENT (\$) 180.00

Complete if Known				
Application Number	09/920,240			
Filing Date	08/01/2001			
First Named Inventor	Pierte ROO			
Examiner Name	Eugene Yun			
Art Unit	2682			
Attorney Docket No.	MP0039.CIP			

METH	OD OF PAY	MENT (check all that a	apply)	FEE CALCULATION (continued)					
Check	Check Credit card Money Other None 3. ADDITIONAL FEES								
Deposit A	Account:	Order L		Large	Entity	Smal	II Entity	<u>′</u>	
Deposit		50.4740		Fe Code	ee \$	F Code	ее \$	<u>Description</u>	Fee Paid
Account		50-1710		1051	130	2051		- Surcharge - late filing fee or oath	
Account Name	KATTEN MU	JCHIN ROSENMAN, LI	LP	1052	50	2052	25	Surcharge - late provisional filing fee or cover sheet	
The Director	is authorized	d to: (check all that ap		1053	130	1053	130	Non-English specification	
	(s) indicated bel	· — ·		1812	2,520	1812	2,520	For filing a request for ex parte reexamination	
		دے Account with any addition		1804	920*	1804	920*	Requesting publication of SIR prior to Examiner action	
	•	FR 1.16 AND/0R 1.17 to m		1805	1,840*	1805	1,840*	Requesting publication of SIR after	
pendency	of this application	ation.		1000	1,010	1000	1,040	Examiner action	
	FEE C	ALCULATION		1251	120	2251	60	Extension for reply within first month	
1. BASIC FI	LING FEE			1252	450	2252	225	Extension for reply within second month	
	Small Entity	B	Fee Paid	1253	1,020	2253	510	Extension for reply within third month	
Fee Code (\$)	Fee Code (\$)	<u>Description</u>	ree Palu	1254	1,590	2254	795	Extension for reply within fourth month	
1001 300 :	2001 150	Utility Filing Fee		1255	2,160	2255	1,080	Extension for reply within fifth month	
1111 500	2111 250	Utility Search Fee		1401	500	2401	250	Notice of Appeal	
1311 200	2311 100	Patent Examination Fee		1402	500	2402	250	Filing a brief in support of an appeal	
l				1403	1,000	2403	500	Request for oral hearing	
				1451	1,510	1451	1,510	Petition to institute a public use proceeding	
	S	UBTOTAL (1) (\$) 0.	00	1452	500	2452	250	Petition to revive - unavoidable (1.17(I))	
2 FYTPA C	'I AIM FEES	FOR UTILITY AND	DEIGGIIE	1453	1,500	2453	750	Petition to revive - unintentional (1.17(m))	
Z. EXTICA C			Fee Paid		1,400	2501		Utility issue fee (or reissue)	
Total Claims	- 20	Extra Claims Fee X 50.00		1502	800	2502		Design issue fee	
Independent	- 3			B	1,100	2503	550	Plant issue fee	
Multiple Depen	ndent	360.00		1460	130	1460	130		
Large Entity	Small Entity	<u> </u>		1807	50	1807	50	Processing fee - provisional app (1.17(q))	180.00
Fee	Fee	Description		1806	180	1806	180	Submission of Information Disclosure Stmt Recording each patent assignment per	100.00
Code (\$) 1202 50	Code (\$) 2202 25	Claims in excess of 20		8021	40	8021	40	property (times number of properties)	
1201 200	2202 23	Independent claims in e	xcess of 3	1809	790	2809	395	Filing a submission after final rejection (1.129(a))	
1203 360	2203 180	Multiple dependent clair	n, if not paid	1814	130	2814	65	Statutory Disclaimer	
1204 200	2204 100	** Reissue independent over original patent	claims	1801	790	2801	395	Request for Continued Examination (RCE)	
1205 50	2205 25	** Reissue claims in exc		1802	900	1802	900	Req for expedited examination - Design App	
		and over original pate	·	Othe	r fee (s	necify)			
			0.00			• •		aid SUBTOTAL (3) (\$) 180	00
** or number previo	ously paid, if great	ter, For Reissues, see above		Reduc	ced by B	asic Filir	ng Fee Pa	aid SUBTOTAL (3) (\$) 180	.00
SUBMITTED	D BY								
Name: And	drew J. Bat	teman	Regi	istratio	n No.:	45	,573	Telephone: (202) 625-354	7
Signature:									

WARNING: As information on this form may become public, credit card information should not be included. Provide credit card information/authorization on PTO-2038.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

OCT 9 3 2005

In re Application of:

Pierte ROO

Application No.: 09/920,240

Filed: August 1, 2001

For: ACTIVE RESISTIVE SUMMER FOR

A TRANSFORMER HYBRID

Examiner: Eugene YUN

Group Art Unit: 2682

Confirmation No.: 4035

Date: October 3, 2005

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In compliance with the duty of disclosure under 37 CFR § 1.56 and the requirements of MPEP § 2001.06(c), and in accordance with the practice under 37 CFR §§ 1.97 and 1.98, the Examiner's attention is directed to the documents listed on the enclosed PTO-1449 and to the copies of the literature references submitted herewith.

In accordance with 37 CFR § 1.97(h), this Information Disclosure Statement is not to be construed as an admission that the information cited is or is considered to be material to patentability as defined in 37 CFR § 1.56(b), nor as an admission that the information constitutes prior art within the meaning of 35 USC §§ 102 and/or 103.

It is respectfully requested that the information listed on the PTO-1449 be considered by the Examiner, and that an initialed copy of the PTO-1449 be returned indicating that such information was considered.

A Fee Transmittal and Credit Card Authorization are being filed concurrently herewith authorizing payment of the \$180.00 IDS fee.

Should the Examiner have any questions, Applicant's undersigned attorney is reachable by telephone in our Washington, D.C. office at (202) 625-3547. The correspondence address of record is provided below.

10/04/2005 SZEWDIE1 00000135 09920240

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180.00 DP

IP Docket Katten Muchin Rosenman, LLP 1025 Thomas Jefferson St., NW East Lobby, Suite 700 Washington, DC 20007-5201 Facsimile No.: (202) 298-7570

Customer No.: 28285

Respectfully submitted, KATTEN MUCHIN ROSENMAN, LLP

By:

Andrew J. Bate/man' Registration No. 45,573



	TO PARTE	T ATTORNEY POOKET NO.	ABBLIGATION		
FORM PTO 1449 MODI J.S. PATENT AND TRA		MP0039.CIP	APPLICATION NO. 09/920,240		
 					
LIST OF RE	FERENCES CITED BY APPLICANT	APPLICANT Pierte ROO			
ATE 01:01:02	LIBRE CANAL CORE	FILING DATE	GROUP		
DATE SUBMITTED TO	USPTO: October 3, 2005	08/01/2001	2682		
OTHER DOCU	MENTS (Including author, title, date, pertinent pag	es, etc.)			
*EXAMINER INITIALS					
	Dunning, Jim, "An All-Digital Phase-Locked Loo Performance Microprocessors," IEEE JOURNAL 1995, pp. 412-422				
	Maneatis, John G. "FA 8.1: Low-Jitter and Prod Techniques"	ess-Independent DLL and F	LL Based on Self-Biase		
	Moon, Yongsam, et al., "An All-Analog Multiphas Wide-Range Operation and Low-Jitter Performa Vol. 35, No. 3, March 2000, pp. 377-384	se Delay-Locked Loop Using nce," IEEE JOURNAL OF So	a Replica Delay Line fo DLID-STATE CIRCUITS		
000000000000000000000000000000000000000	Wang, HongMo, "WP 23.8: A 9.8GHz Back-Gat 20, Paper WP 23.0, 1999 IEEE INTERNATIONAL				
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	Rudell, et al., "SA 18.3: A 1.9 GHz Wide-Bank I Cordless Telephone Applications, " 1997, pp. 30		Integrated Receiver for		
	Garlepp, Bruno W., et al., "18.3 A Portable Digital	al DLL Architecture for CMO			
	SYMPOSIUM ON VLSI CIRCUITS DIGEST OF	TECHNICAL PAPERS," pp.	214-215		
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	/Eugene Yun/ / DATE CONSI	DERED 02/20/			
EXAMINER		25 DE 20 10 10 10 10 10 10 10 10 10 10 10 10 10			

SHEET 1 OF 1